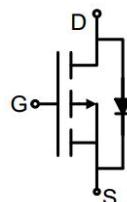
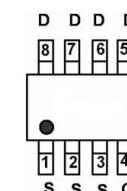
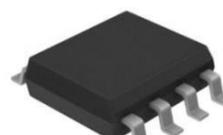


P-Channel Enhancement Mode Power MOSFET

<p>Description</p> <p>The GT2K6P15S uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.</p> <p>General Features</p> <ul style="list-style-type: none"> ● V_{DS} -150V ● I_D (at $V_{GS} = -10V$) -2A ● $R_{DS(ON)}$ (at $V_{GS} = -10V$) < 260mΩ ● 100% Avalanche Tested ● RoHS Compliant <p>Application</p> <ul style="list-style-type: none"> ● Power switch ● DC/DC converters 	 <p>Schematic diagram</p>  <p>pin assignment</p>  <p>SOP-8</p>
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Ordering Information			
Device	Package	Marking	Packaging
GT2K6P15S	SOP-8	GT2K6P15	4000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ C$, unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-150	V
Continuous Drain Current	$T_C = 25^\circ C$	I_D	-2	A
	$T_C = 100^\circ C$		-1.3	
Pulsed Drain Current	(note1)	I_{DM}	-8	A
Gate-Source Voltage		V_{GS}	± 20	V
Power Dissipation		P_D	1.8	W
Single pulse avalanche energy	(note2)	E_{AS}	56	mJ
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 To 150	°C

Thermal Resistance				
Parameter		Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient		R_{thJA}	69	°C/W

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-150	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -150\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	-1	μA
Gate-Source Leakage	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1.0	-2.5	-3.0	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -2\text{A}$	--	210	260	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}} = -5\text{V}, I_D = -2\text{A}$	--	14	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -75\text{V}, f = 1.0\text{MHz}$	--	1400	--	pF
Output Capacitance	C_{oss}		--	60	--	
Reverse Transfer Capacitance	C_{rss}		--	1	--	
Total Gate Charge	Q_g	$V_{\text{DD}} = -75\text{V}, I_D = -2\text{A}, V_{\text{GS}} = -10\text{V}$	--	36	--	nC
Gate-Source Charge	Q_{gs}		--	4	--	
Gate-Drain Charge	Q_{gd}		--	8	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{\text{DD}} = -75\text{V}, I_D = -2\text{A}, R_G = 1.6\Omega$	--	26	--	ns
Turn-on Rise Time	t_r		--	7	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	28	--	
Turn-off Fall Time	t_f		--	12	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_s	$T_C = 25^\circ\text{C}$	--	--	-2	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{SD} = -2\text{A}, V_{GS} = 0\text{V}$	--	--	-1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = -2\text{A}, V_{GS} = 0\text{V}$ $dI/dt = -100\text{A/us}$	--	150	--	nC
Reverse Recovery Time	T_{rr}		--	37	--	ns

Notes

- Repetitive Rating: Pulse width limited by maximum junction temperature
- EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=-50\text{V}$, $V_{GS}=-10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$
The table shows the minimum avalanche energy, which is 156mJ when the device is tested until failure
- Identical low side and high side switch with identical R_G

Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

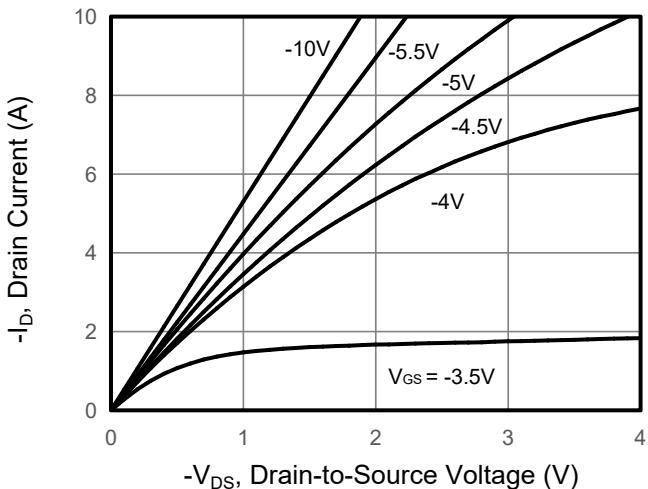


Figure 2. Transfer Characteristics

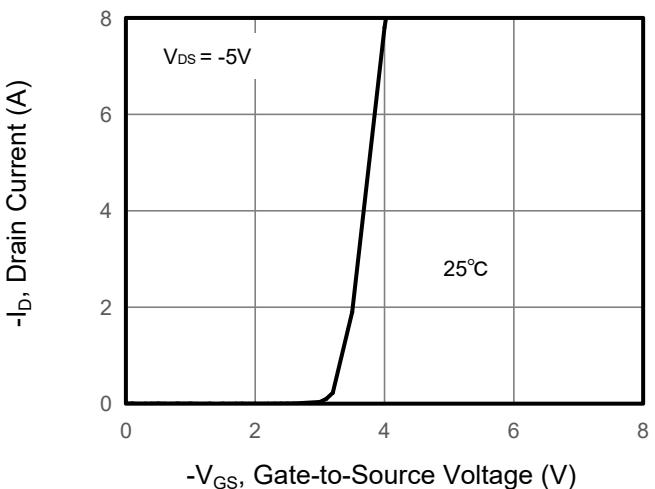


Figure 3. Drain Source On Resistance

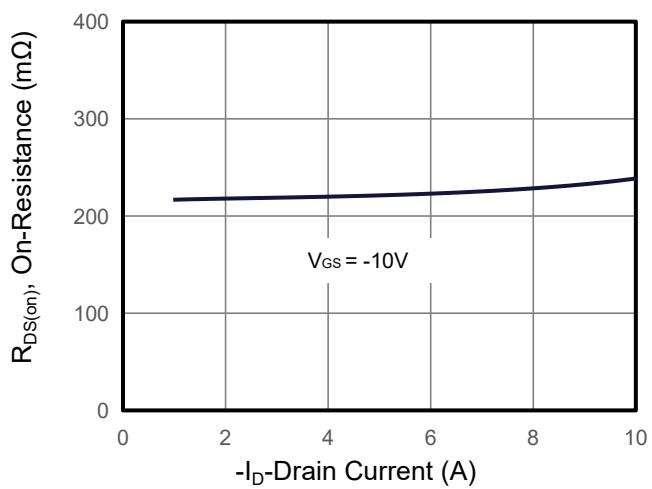


Figure 4. Gate Charge

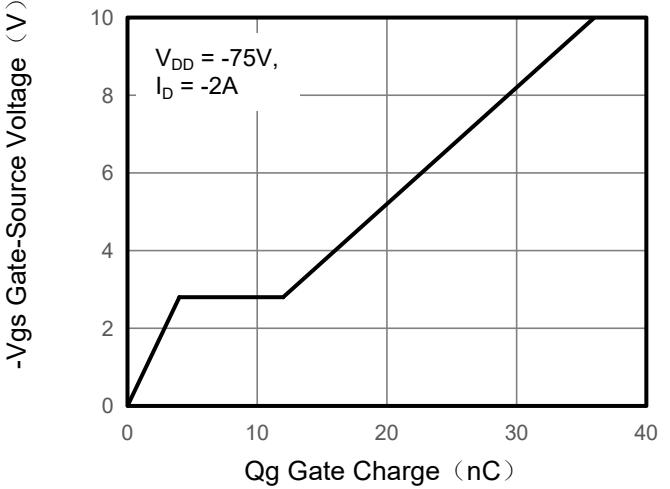


Figure 5. Capacitance

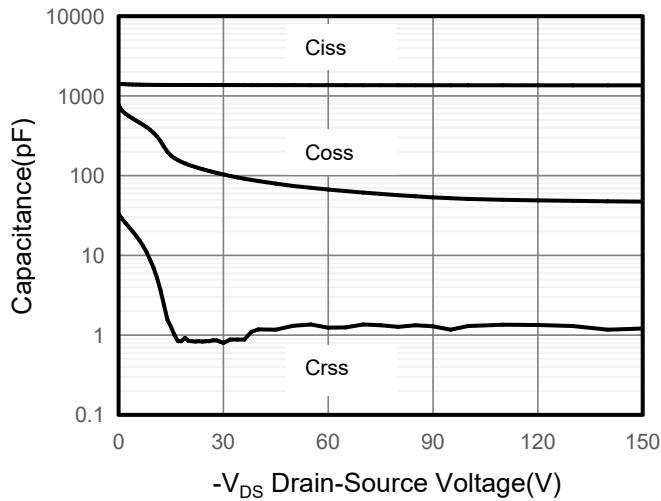
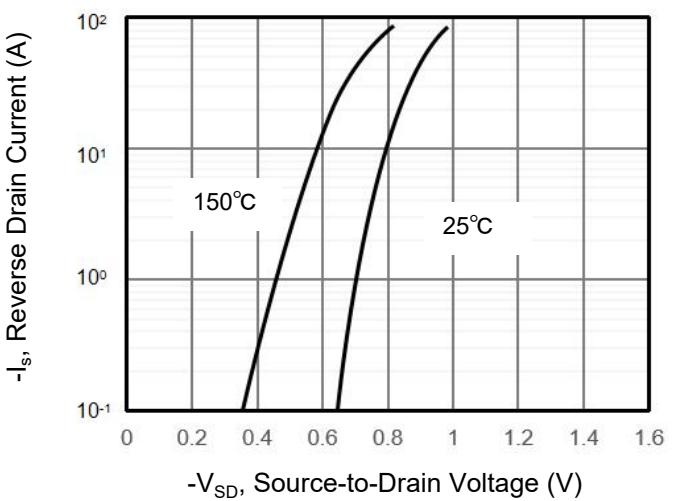


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

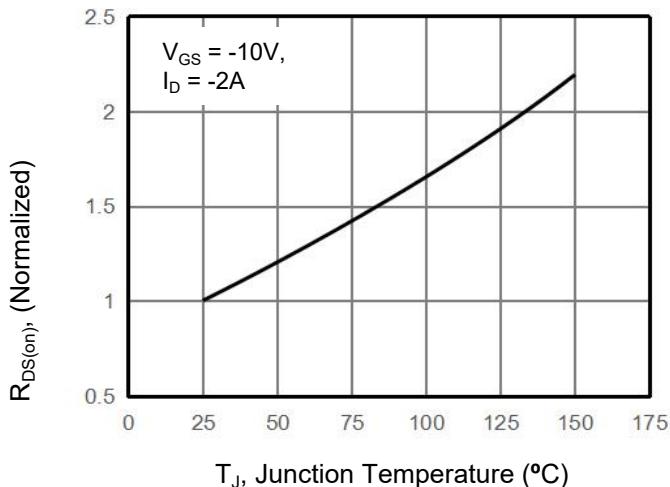


Figure 8. Safe Operation Area

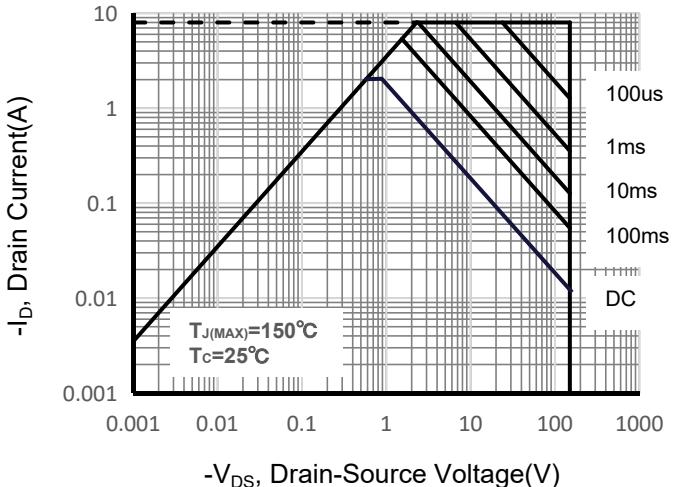


Figure 9. Maximum Continuous Drain Current vs Case Temperature

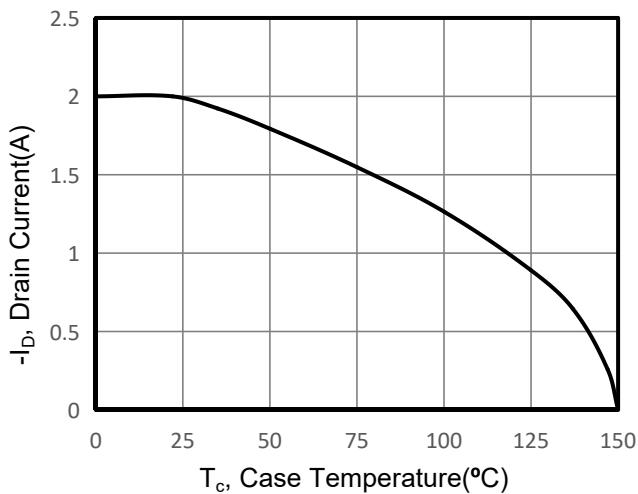
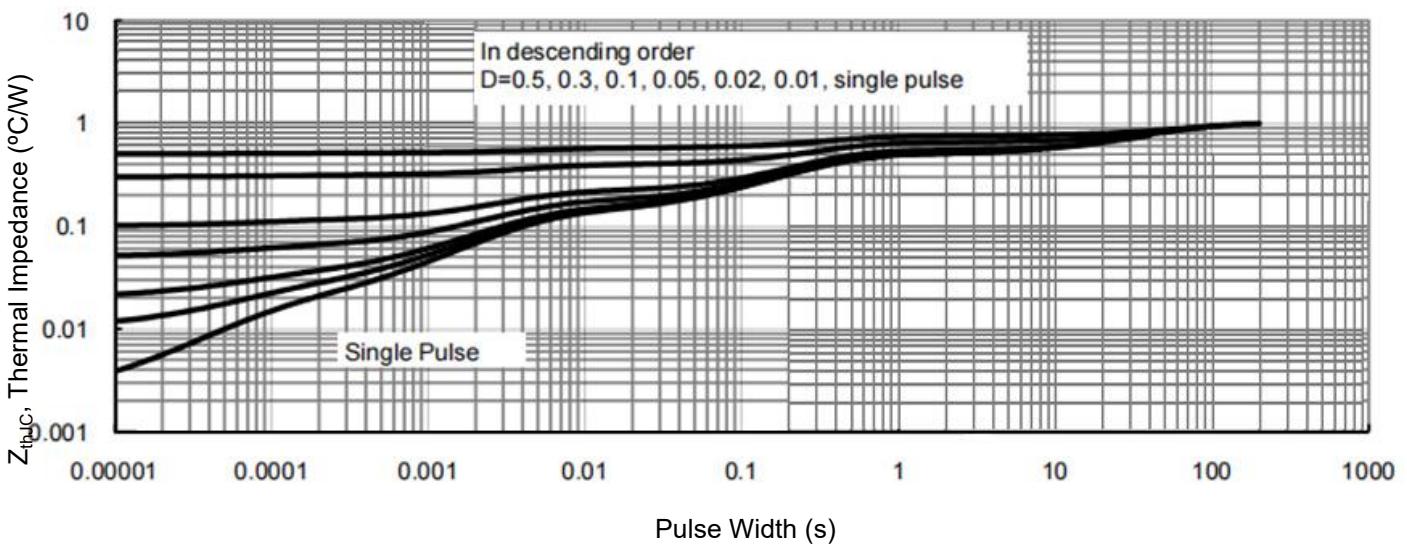
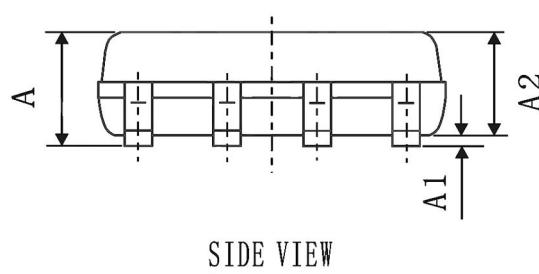
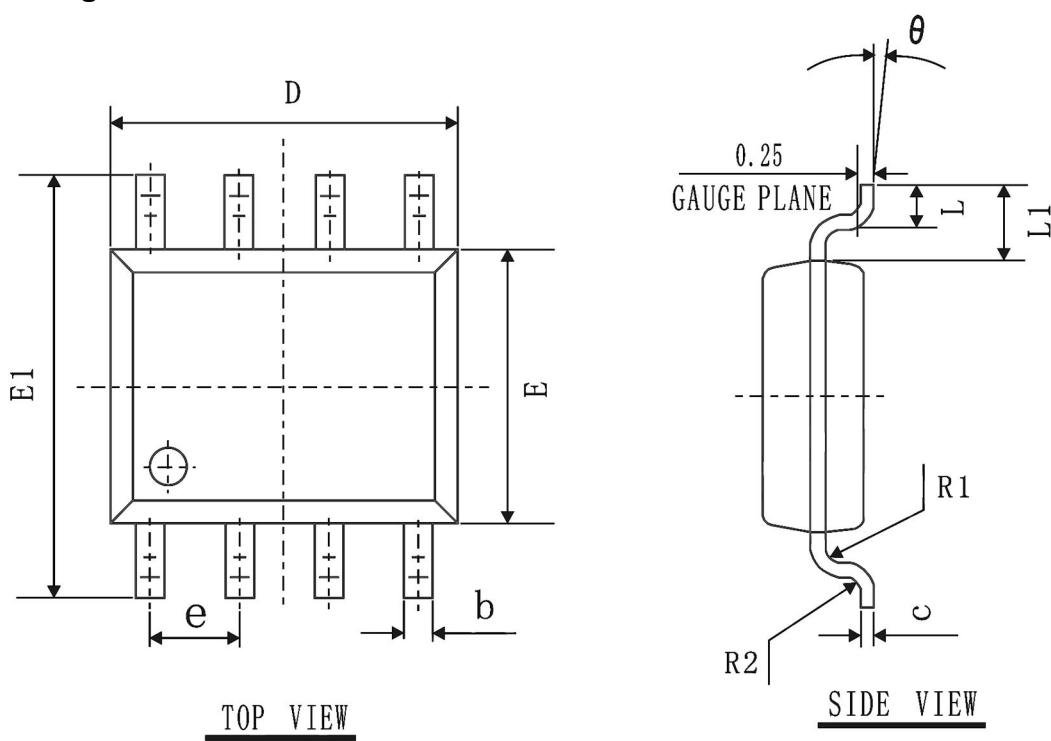


Figure 10. Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	1.40	1.60	1.80
A1	0.05	0.15	0.25
A2	1.35	1.45	1.55
b	0.30	0.40	0.50
c	0.153	0.203	0.253
D	4.80	4.90	5.00
E	3.80	3.90	4.00
E1	5.80	6.00	6.20
L	0.45	0.70	1.00
θ	2°	4°	6°
L1		1.04 REF	
e		1.27 BSC	
R1		0.07 TYP	
R2		0.07 TYP	