

## N-Channel Enhancement Mode Power MOSFET

### Description

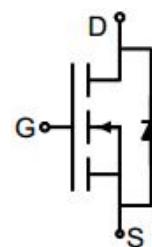
The GT025N06AQ uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. It can be used in a wide variety of applications.

### General Features

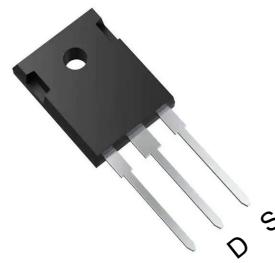
- $V_{DS}$  60V
- $I_D$  (at  $V_{GS} = 10V$ ) 175A
- $R_{DS(ON)}$  (at  $V_{GS} = 10V$ ) < 2.5mΩ
- $R_{DS(ON)}$  (at  $V_{GS} = 4.5V$ ) < 3.2mΩ
- 100% Avalanche Tested
- RoHS Compliant

### Application

- Power switch
- DC/DC converters
- Synchronous Rectification



Schematic Diagram



TO-247

### Ordering Information

Device	Package	Marking	Packaging
GT025N06AQ	TO-247	GT025N06	30pcs/Tube

### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Continuous Drain Current $T_C = 25^\circ\text{C}$	$I_D$	175	A
$T_C = 100^\circ\text{C}$		110	
Pulsed Drain Current (note1)	$I_{DM}$	700	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Single pulse avalanche energy (note2)	$E_{AS}$	420	mJ
Power Dissipation	$P_D$	220	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C

### Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{thJA}$	50	°C/W
Maximum Junction-to-Case	$R_{thJC}$	0.57	°C/W

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	60	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = 60V, V_{GS} = 0V$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{GS} = \pm 20V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.7	2.5	V
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10V, I_D = 20\text{A}$	--	2.1	2.5	$\text{m}\Omega$
		$V_{GS} = 4.5V, I_D = 20\text{A}$	--	2.6	3.2	
Forward Transconductance	$g_{\text{FS}}$	$V_{DS} = 5V, I_D = 20\text{A}$	--	95	--	s
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{GS} = 0V,$ $V_{DS} = 30V,$ $f = 1.0\text{MHz}$	--	5400	--	pF
Output Capacitance	$C_{\text{oss}}$		--	1350	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	70	--	
Total Gate Charge	$Q_g$	$V_{DD} = 30V,$ $I_D = 20\text{A},$ $V_{GS} = 10V$	--	70	--	nC
Gate-Source Charge	$Q_{gs}$		--	21	--	
Gate-Drain Charge	$Q_{gd}$		--	16	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{DD} = 30V,$ $I_D = 20\text{A},$ $R_G = 3\Omega$	--	16	--	ns
Turn-on Rise Time	$t_r$		--	9	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	36	--	
Turn-off Fall Time	$t_f$		--	11	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_s$	$T_c = 25^\circ\text{C}$	--	--	175	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 20\text{A}, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = 20\text{A}, V_{GS} = 0V$ $di/dt = 500\text{A}/\mu\text{s}$	--	150	--	nC
Reverse Recovery Time	$Tr$		--	30	--	ns

**Notes**

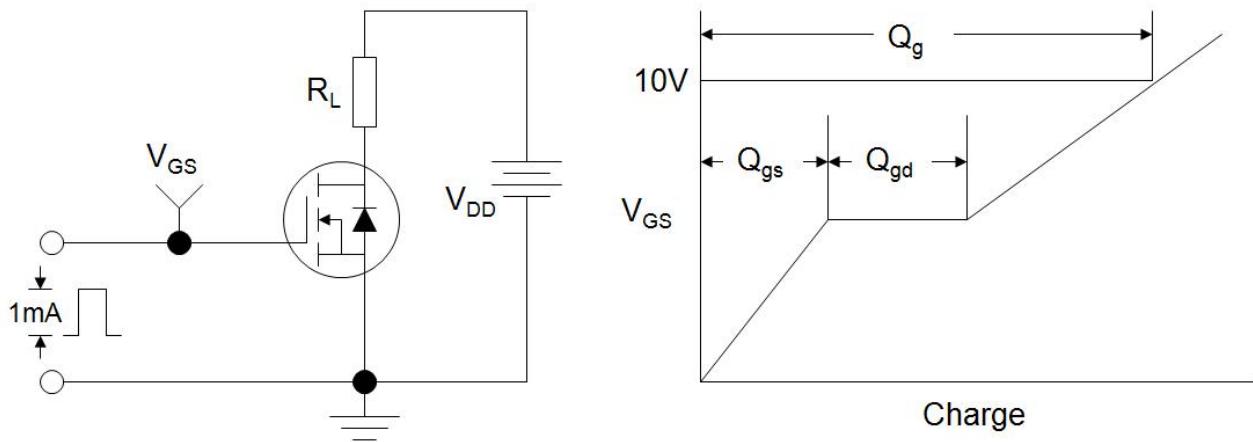
1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. EAS condition :  $T_j=25^\circ\text{C}$ ,  $V_{DD}=50V$ ,  $V_{GS}=10V$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$

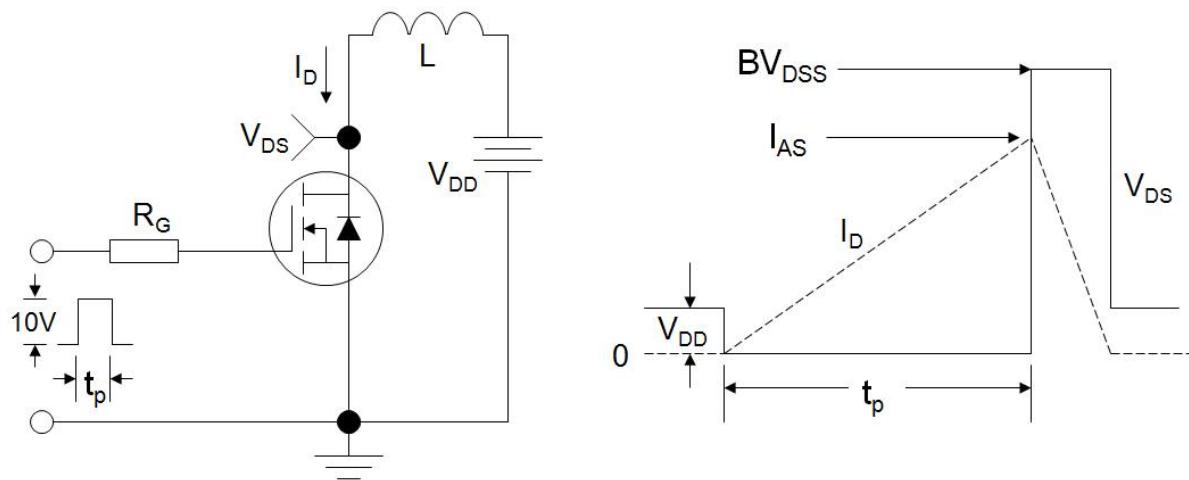
The table shows the minimum avalanche energy, which is 1156mJ when the device is tested until failure

3. Identical low side and high side switch with identical  $R_g$

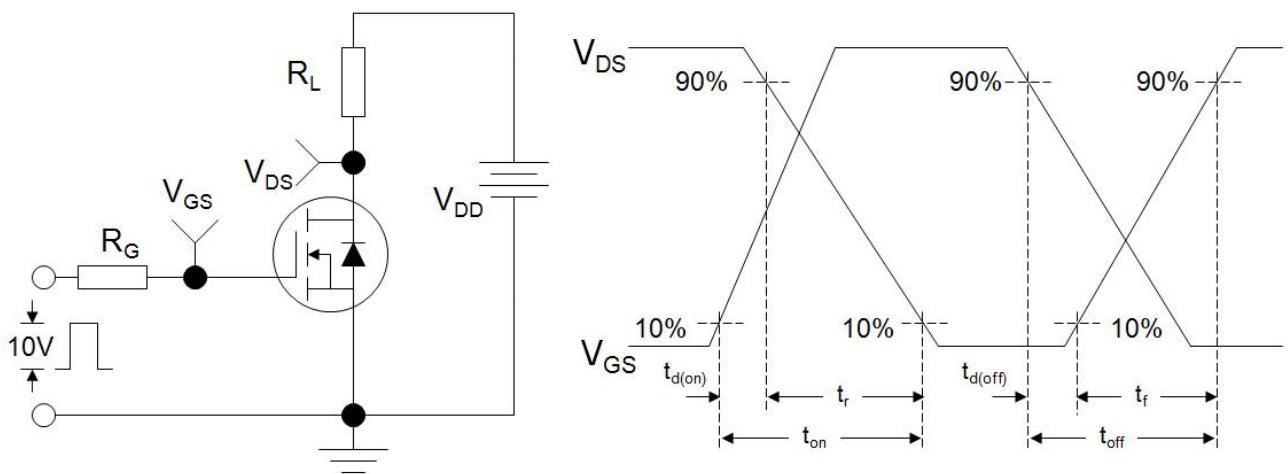
### Gate Charge Test Circuit



### EAS Test Circuit

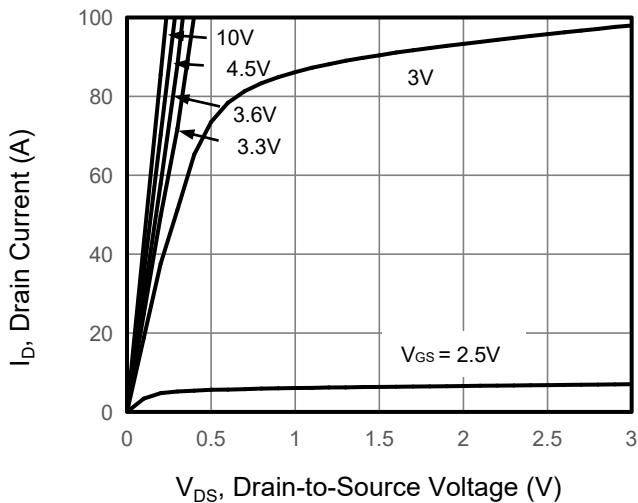


### Switch Time Test Circuit

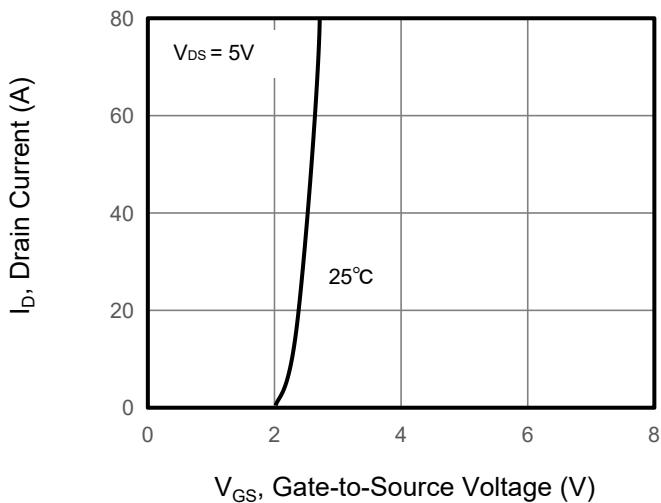


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

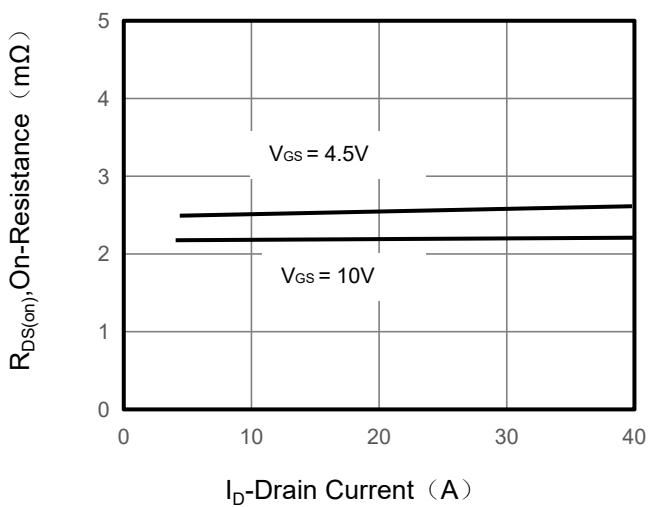
**Figure 1. Output Characteristics**



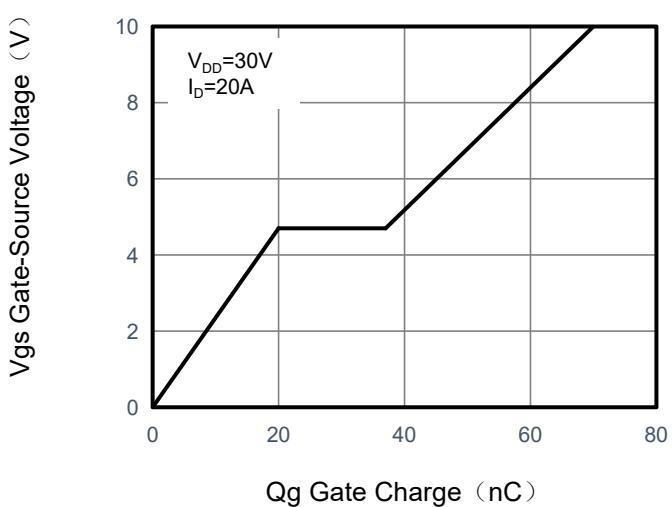
**Figure 2. Transfer Characteristics**



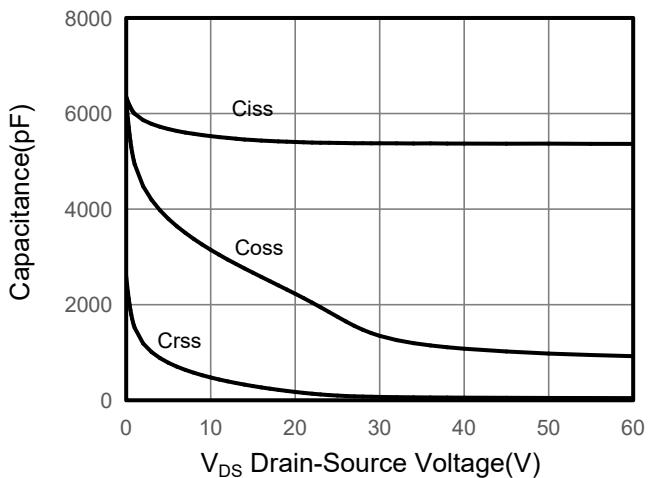
**Figure 3. Drain Source On Resistance**



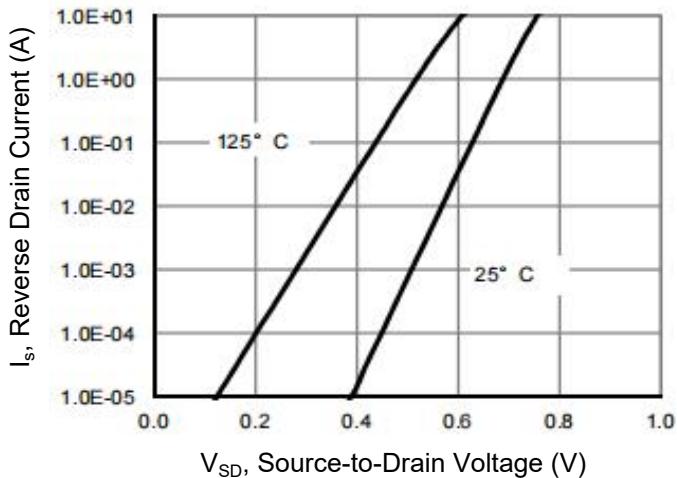
**Figure 4. Gate Charge**



**Figure 5. Capacitance vs Vds**

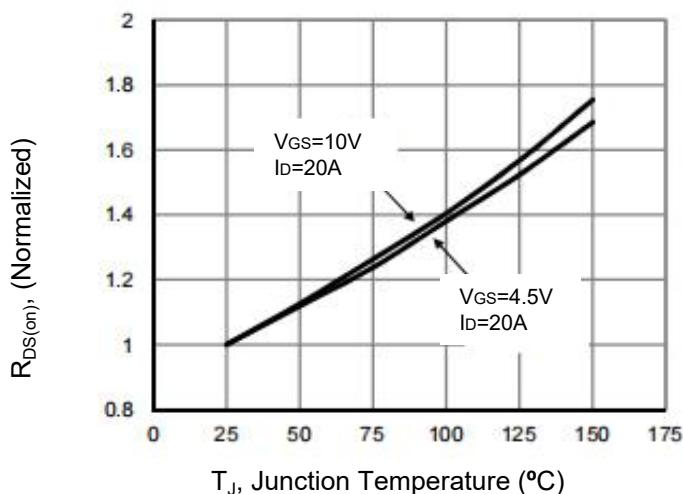


**Figure 6. Source-Drain Diode Forward**

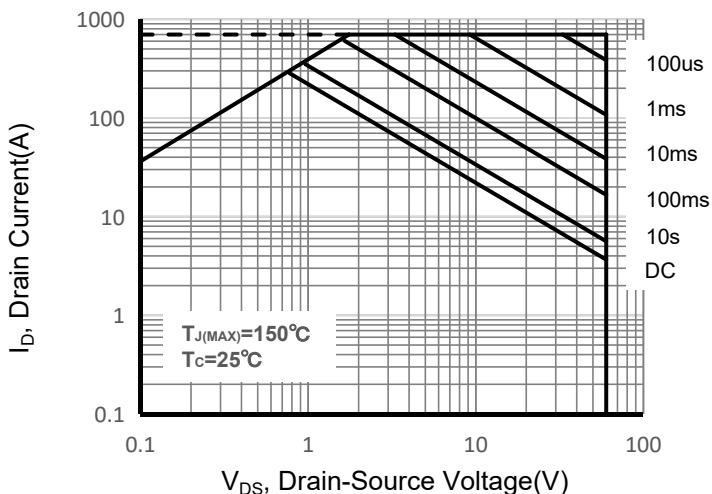


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

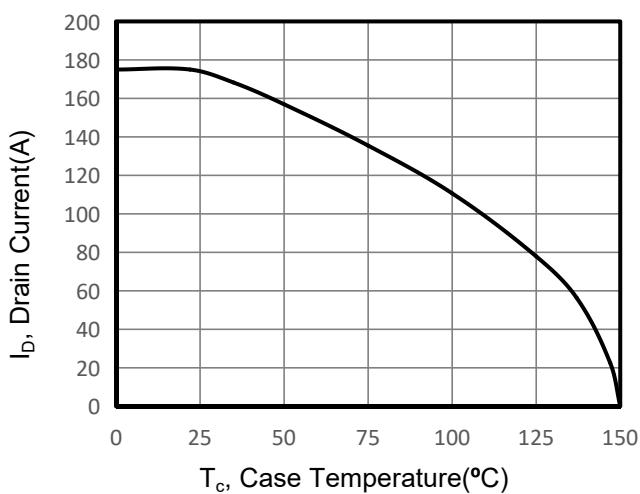
**Figure 7. Drain-Source On-Resistance**



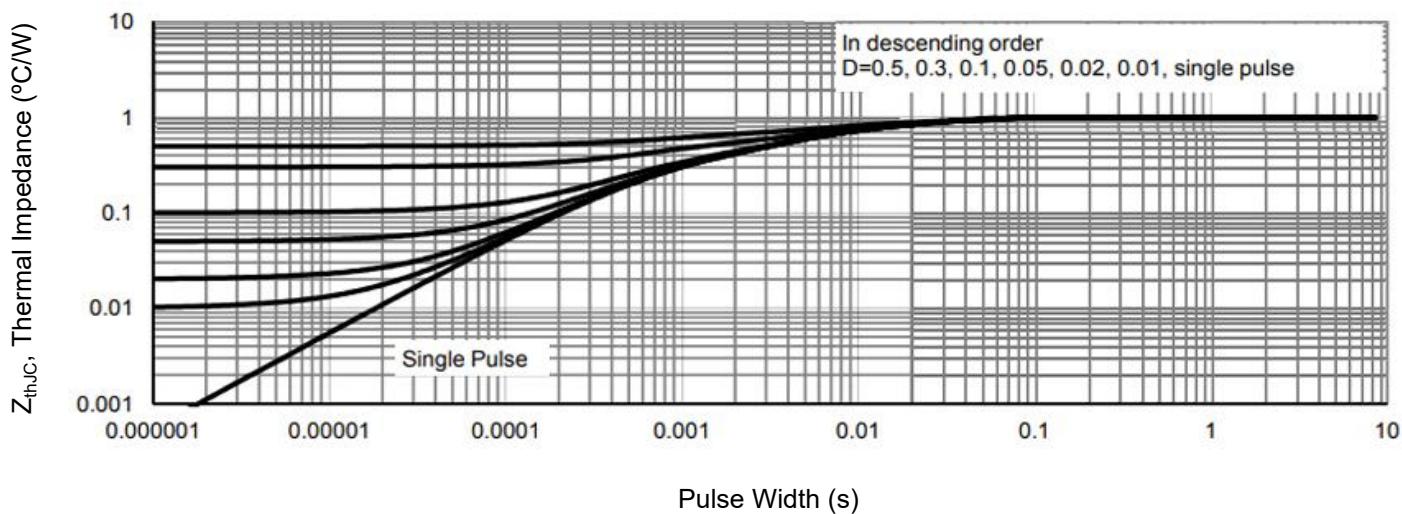
**Figure 8. Safe Operation Area**



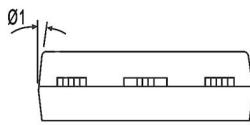
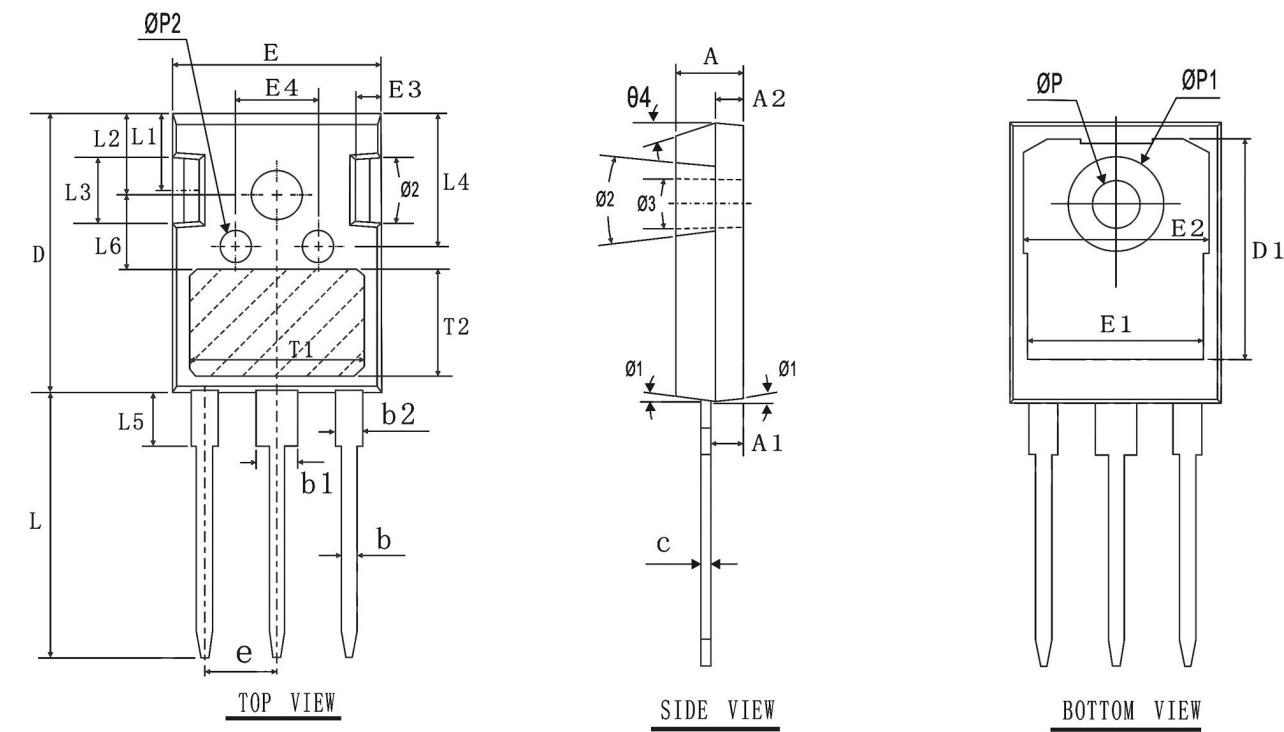
**Figure 9. Maximum Continuous Drain Current vs Case Temperature**



**Figure 10. Normalized Maximum Transient Thermal Impedance**



## TO-247 Package Information



COMMON DIMENSIONS  
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.20	2.40	2.60
A2	1.85	2.00	2.15
b	1.10	1.20	1.30
b1	2.80	3.00	3.20
b2	1.80	2.00	2.20
C	0.52	0.62	0.72
D	20.35	20.65	20.95
D1	16.35	16.55	16.75
E	15.50	15.80	16.10
E1	13.10	13.30	13.50
E2	13.80	14.00	14.20
E3	1.45	1.60	1.75
E4	6.00	6.20	6.40
L	19.80	20.00	20.20
L1	5.88	5.98	6.08
L2	5.88	5.98	6.08
L3	4.90	5.00	5.10
L4	9.70	9.80	9.90
L5	4.10	4.30	4.50
Φ1	4°	7°	10°
Φ2	11°	14°	17°
Φ3	1°	--	2°
Φ4	10°	15°	20°
ΦP	3.35	3.60	3.85
ΦP1	--	--	7.30
ΦP2	2.25	2.50	2.75
e	5.44BSC		
T1	12.80REF		
T2	7.80REF		
L6	5.50REF		