

**N-Channel Enhancement Mode Power MOSFET****Description**

The GT010N10TLA uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. It can be used in a wide variety of applications.

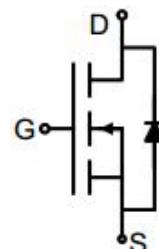
AEC-Q101 Qualified

**General Features**

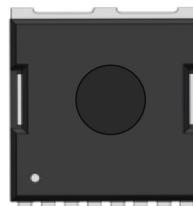
- $V_{DS}$  100V
- $I_D$  (at  $V_{GS} = 10V$ ) 400A
- $R_{DS(ON)}$  (at  $V_{GS} = 10V$ ) < 1.35mΩ
- 100% Avalanche Tested
- RoHS Compliant

**Application**

- Power switch
- DC/DC converters



Schematic diagram



TOLL-8L

**Ordering Information**

Device	Package	Marking	Packaging
GT010N10TLA	TOLL-8L	GT010N10	2000pcs/Reel

**Absolute Maximum Ratings**  $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter		Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	100	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	400	A
	$T_C = 100^\circ\text{C}$		287	
Pulsed Drain Current	(note1)	$I_{DM}$	1600	A
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Power Dissipation		$P_D$	480	W
Single pulse avalanche energy	(note2)	$E_{AS}$	1056	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 To 175	°C

**Thermal Resistance**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	50	°C/W
Thermal Resistance, Junction-to-Case	$R_{thJC}$	0.32	°C/W

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.0	3.0	4.0	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 50\text{A}$	--	1.15	1.35	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{GS}} = 5\text{V}, I_D = 50\text{A}$	--	58	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 50\text{V}, f = 0.2\text{MHz}$	--	13200	--	pF
Output Capacitance	$C_{\text{oss}}$		--	4220	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	55	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 50\text{V}, I_D = 50\text{A}, V_{\text{GS}} = 10\text{V}$	--	220	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	53	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	54	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 50\text{V}, I_D = 50\text{A}, R_G = 1.6\Omega$	--	40	--	ns
Turn-on Rise Time	$t_r$		--	23	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	67	--	
Turn-off Fall Time	$t_f$		--	22	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	400	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 50\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = 50\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A}/\mu\text{s}$	--	325	--	nC
Reverse Recovery Time	$T_{\text{rr}}$		--	128	--	ns

**Notes**

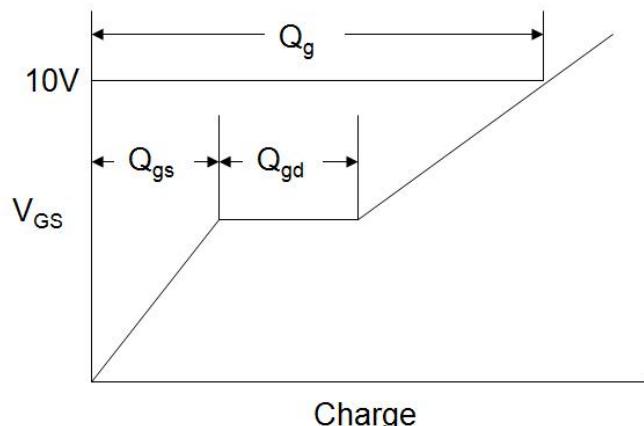
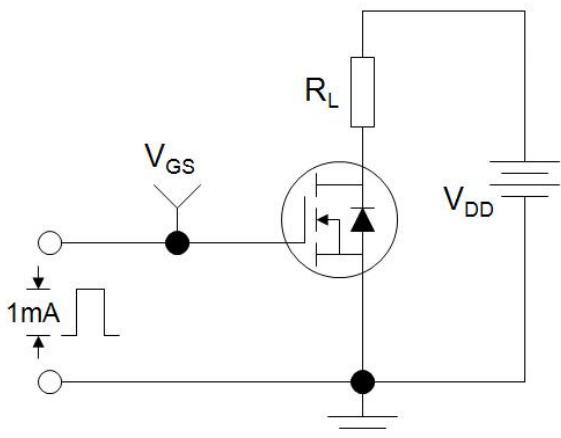
1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$

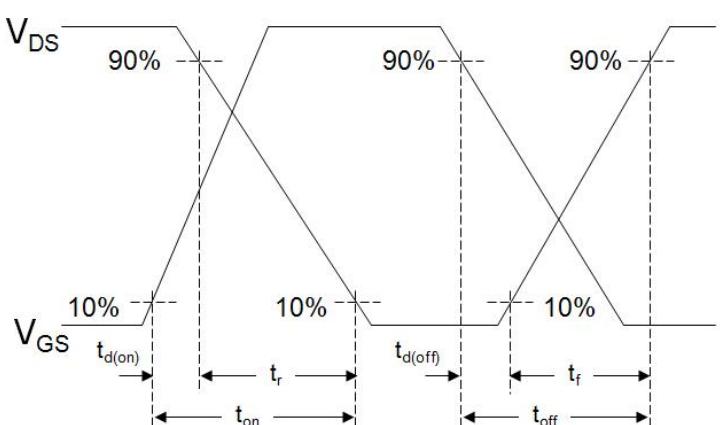
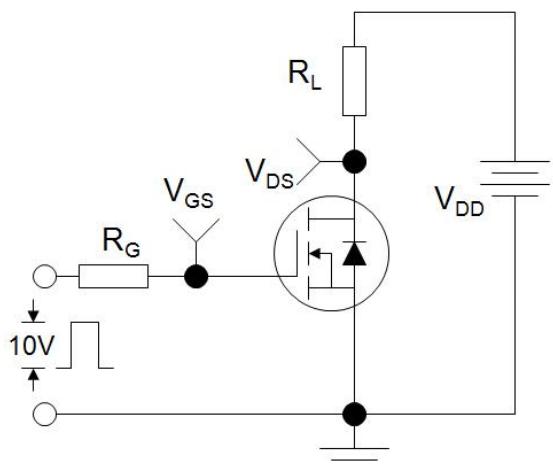
The table shows the minimum avalanche energy, which is 2916mJ when the device is tested until failure

3. Identical low side and high side switch with identical  $R_G$

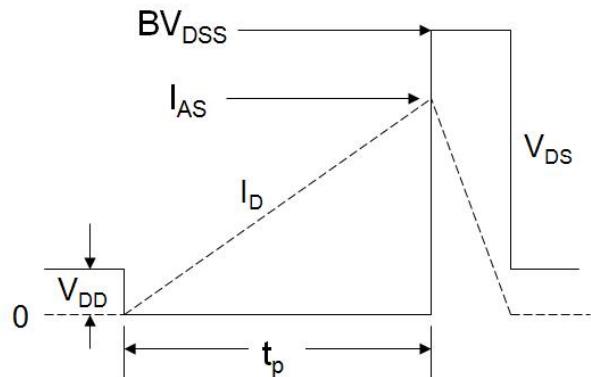
### Gate Charge Test Circuit



### Switch Time Test Circuit

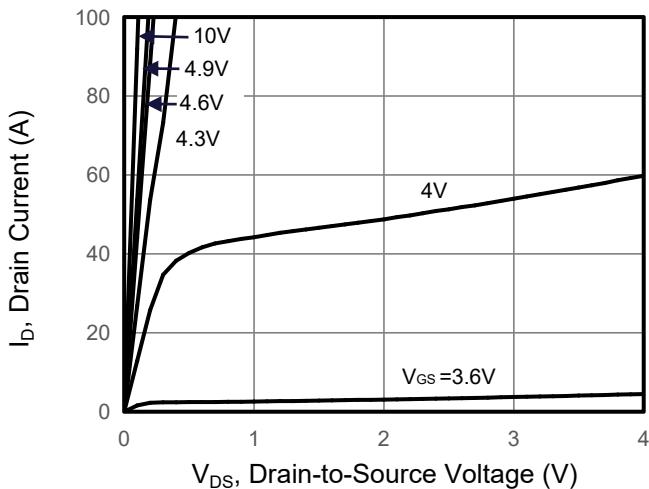


### EAS Test Circuit

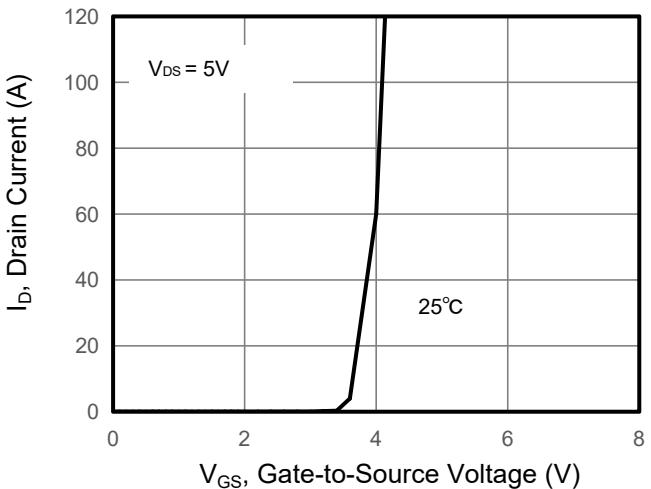


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

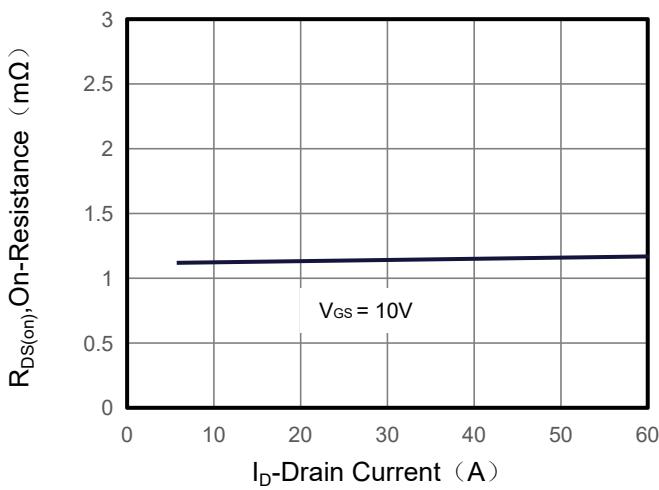
**Figure 1. Output Characteristics**



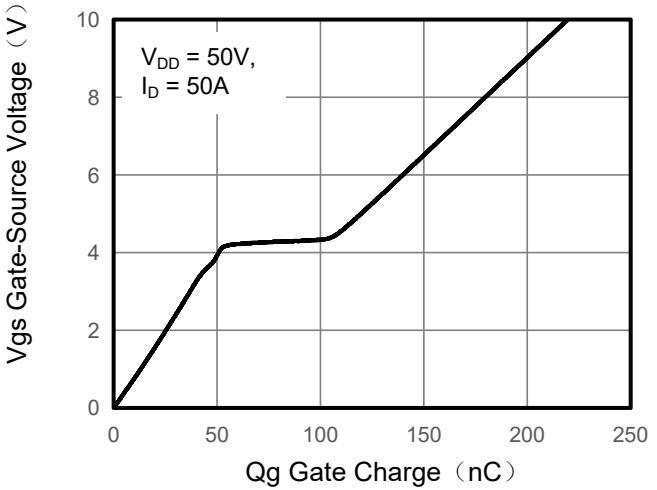
**Figure 2. Transfer Characteristics**



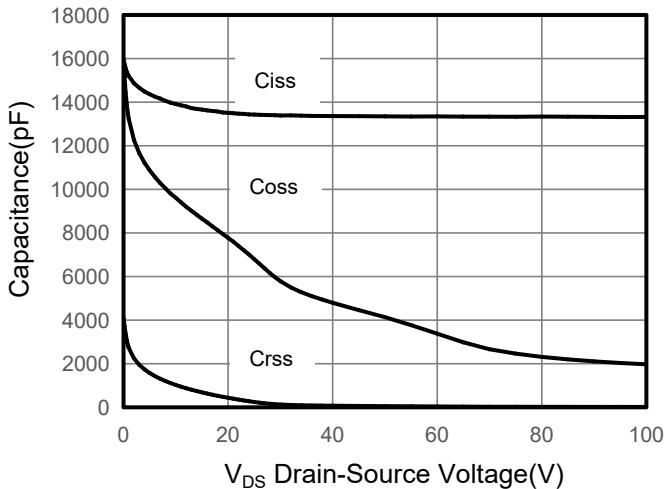
**Figure 3. Drain Source On Resistance**



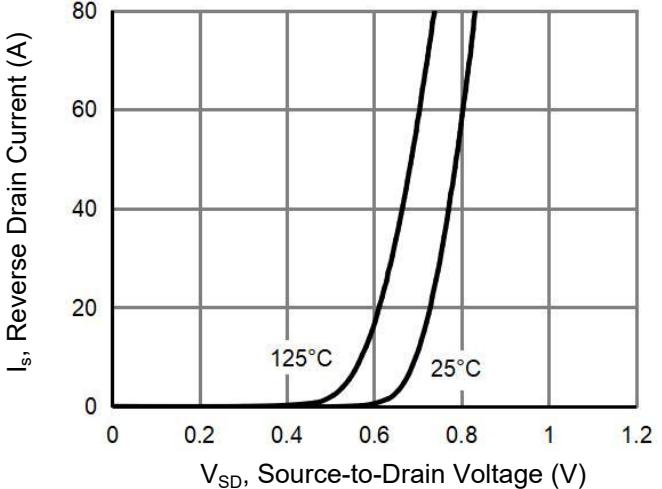
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

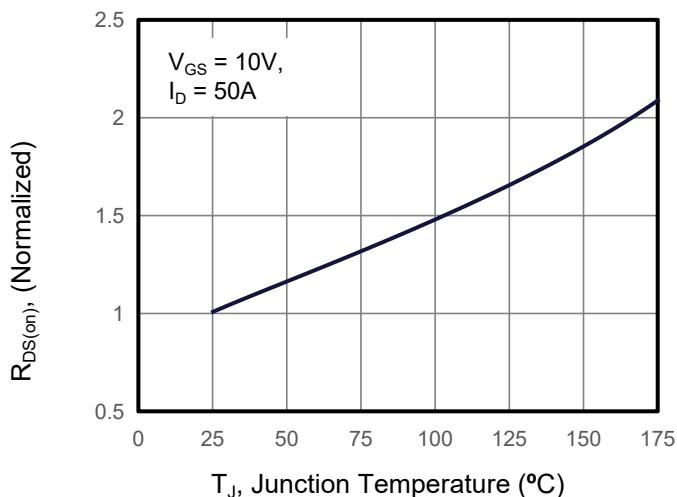


**Figure 6. Source-Drain Diode Forward**

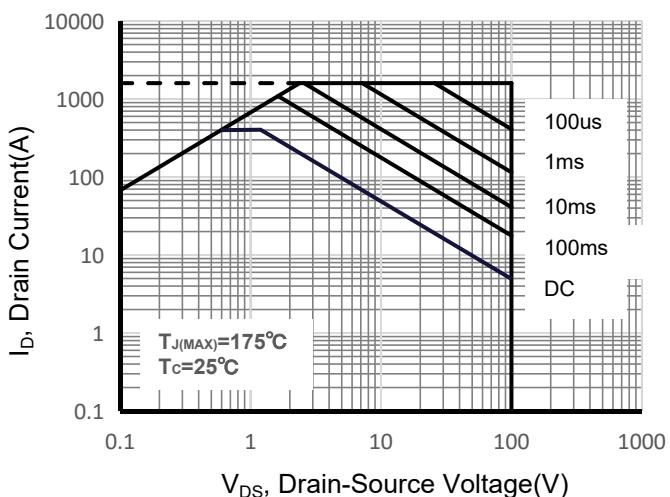


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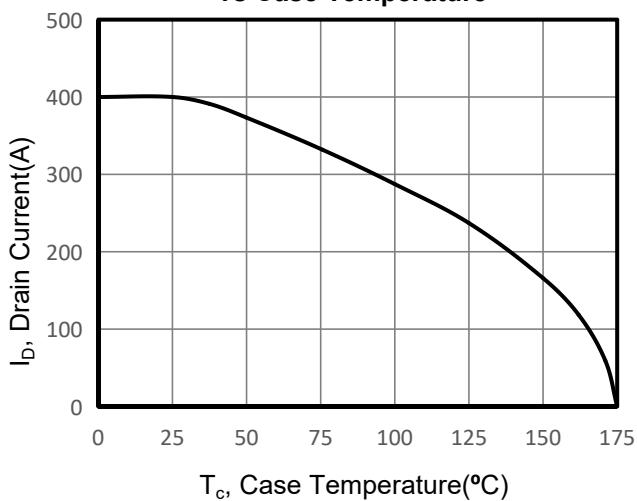
**Figure 7. Drain-Source On-Resistance**



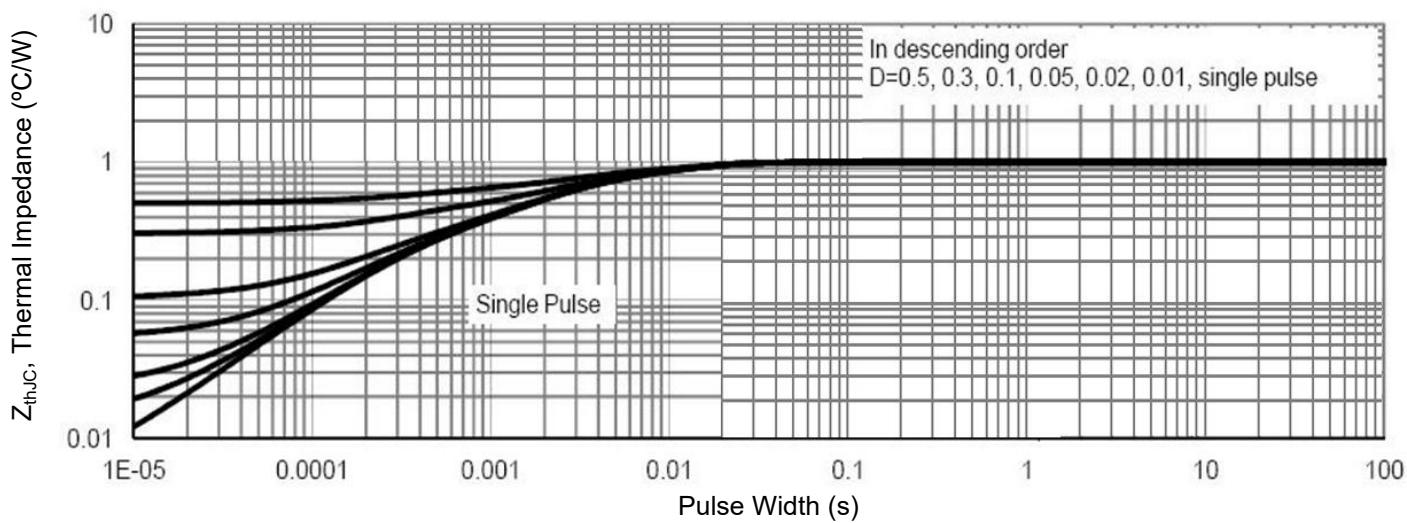
**Figure 8. Safe Operation Area**



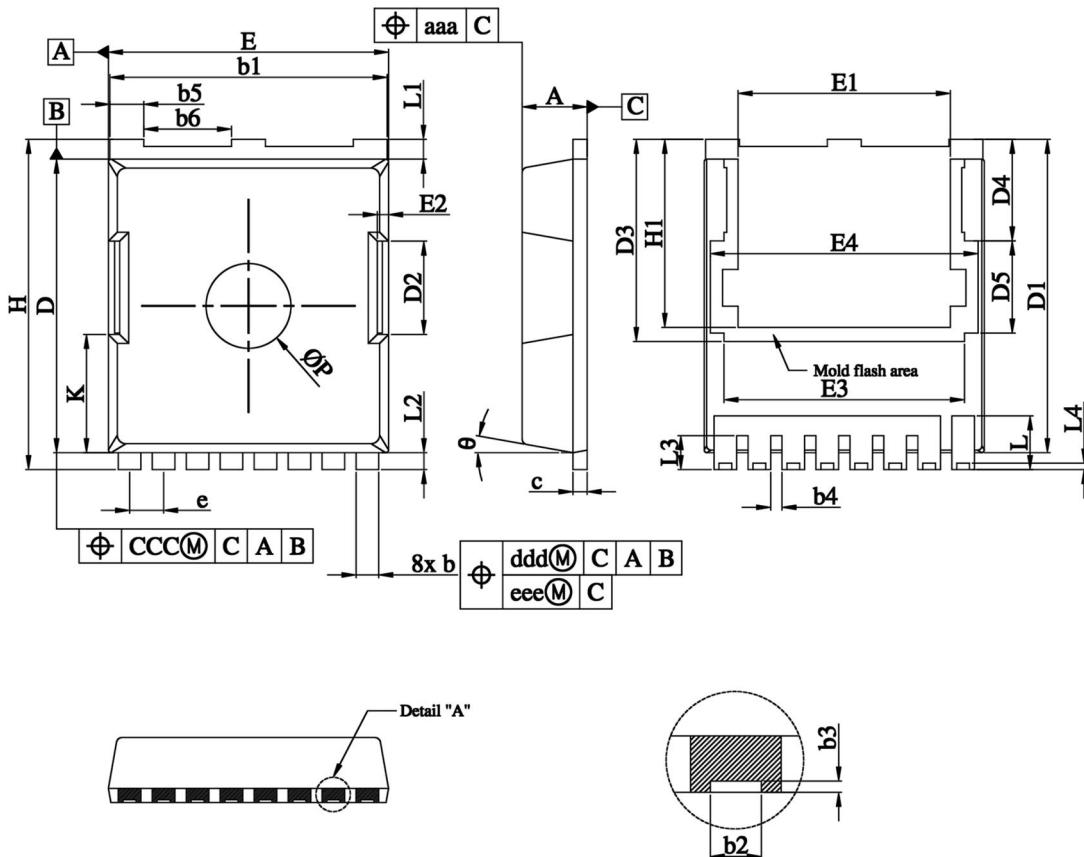
**Figure 9. Maximum Continuous Drain Current vs Case Temperature**



**Figure 10. Normalized Maximum Transient Thermal Impedance**



## TOLL-8L Package Information



SYMBOL	COMMON			SYMBOL	COMMON			
	MILLIMETER				MILLIMETER			
	MIN.	NOMINAL	MAX.		MIN.	NOMINAL	MAX.	
A	2.20	2.30	2.40	E2	0.30	0.40	0.50	
b	0.70	0.80	0.90	E3		8.50		
b1	9.70	9.80	9.90	E4		9.46		
b2	0.36	0.45	0.55	H	11.50	11.68	11.85	
b3	0.05	0.100	/	H1	6.55	6.65	6.75	
b4	0.30	0.40	0.50	K	4.08	4.18	4.28	
b5	1.10	1.20	1.30	L	1.60	1.90	2.10	
b6	3.00	3.10	3.20	L1	0.50	0.70	0.90	
c	0.40	0.50	0.60	L2	0.50	0.60	0.70	
D	10.28	10.38	10.55	L3	1.00	1.20	1.30	
D1	10.98	11.08	11.18	L4	0.13	0.23	0.33	
D2	3.20	3.30	3.40	P	2.85	3.00	3.15	
D3	7.15			θ	10° REF			
D4	3.59			aaa	0.20			
D5	3.26			ccc	0.20			
e	1.10	1.20	1.30	ddd	0.25			
E	9.80	9.90	10.00	eee	0.20			
E1	7.40	7.50	7.60					