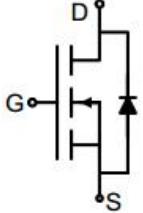
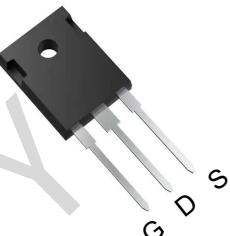


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GC040N65QF uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 650V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 72A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 42mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> <li>● Ultra-fast body diode</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TO-247</p>		
<b>Ordering Information</b>			
Device	Package	Marking	Packaging
GC040N65QF	TO-247	GC040N65F	30pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted					
Parameter	Symbol	Value	Unit		
Drain-Source Voltage	$V_{DS}$	650	V		
Continuous Drain Current $T_C = 25^\circ\text{C}$	$I_D$	72	A		
		45			
Pulsed Drain Current (note1)	$I_{DM}$	216	A		
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V		
Power Dissipation	$P_D$	496	W		
Single pulse avalanche energy (note2)	$E_{AS}$	1000	mJ		
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	°C		

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	46	°C/W	
Maximum Junction-to-Case	$R_{thJC}$	0.25	°C/W	

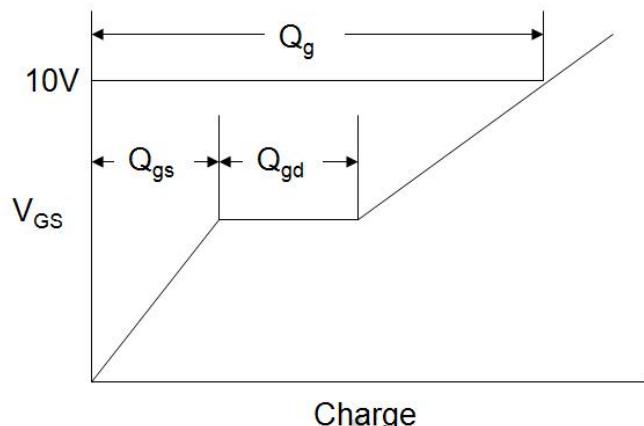
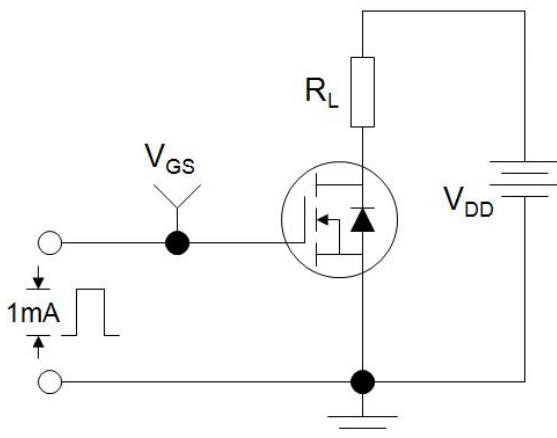
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	5	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 30\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.2	--	4.6	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 35\text{A}$	--	36	42	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = 20\text{V}, I_D = 35\text{A}$	--	51	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 100\text{V}, f = 100\text{KHz}$	--	4900	--	pF
Output Capacitance	$C_{\text{oss}}$		--	265	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	1.6	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 480\text{V}, I_D = 35\text{A}, V_{\text{GS}} = 10\text{V}$	--	158	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	41	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	90	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 400\text{V}, I_D = 35\text{A}, R_G = 27\Omega$	--	128	--	ns
Turn-on Rise Time	$t_r$		--	112	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	400	--	
Turn-off Fall Time	$t_f$		--	93	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	72	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 35\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = 35\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A}/\mu\text{s}$	--	0.81	--	$\mu\text{C}$
Reverse Recovery Time	$T_{\text{rr}}$		--	130	--	ns

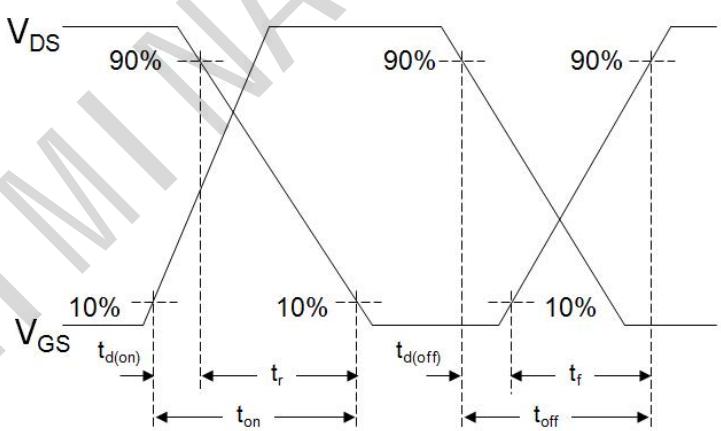
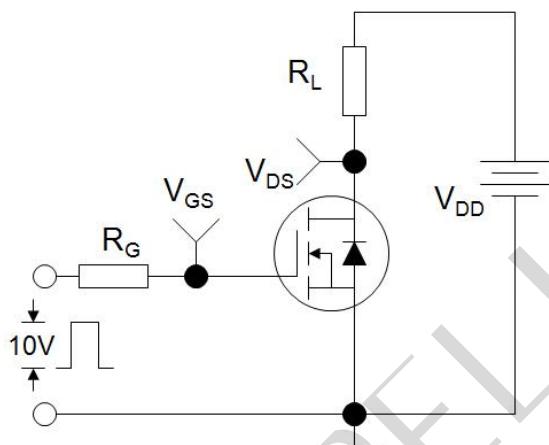
**Notes**

- Repetitive Rating: Pulse width limited by maximum junction temperature
- EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=30\text{mH}$ ,  $R_G=25\Omega$
- Identical low side and high side switch with identical  $R_G$

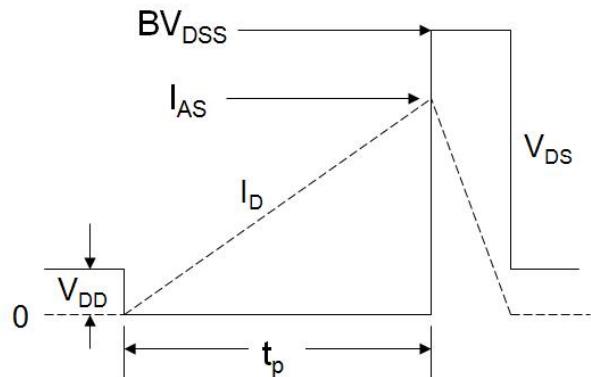
### Gate Charge Test Circuit



### Switch Time Test Circuit

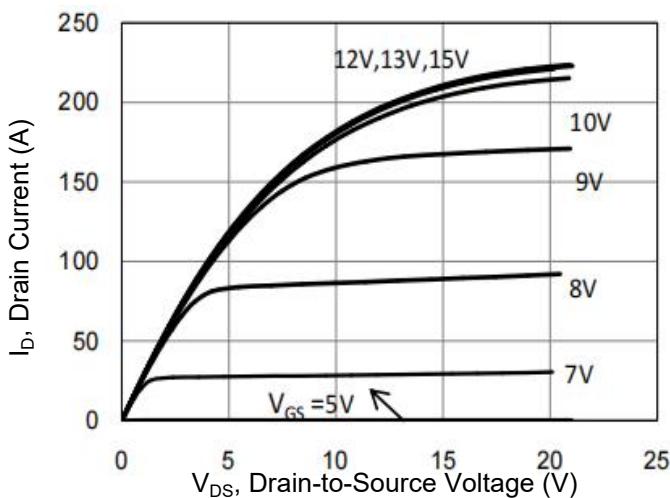


### EAS Test Circuit

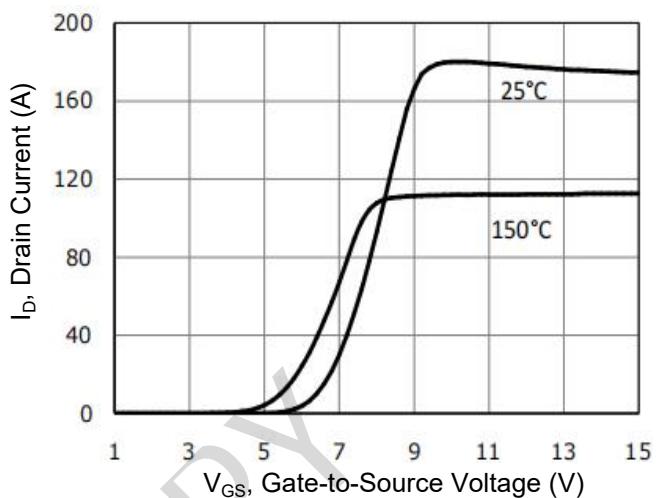


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

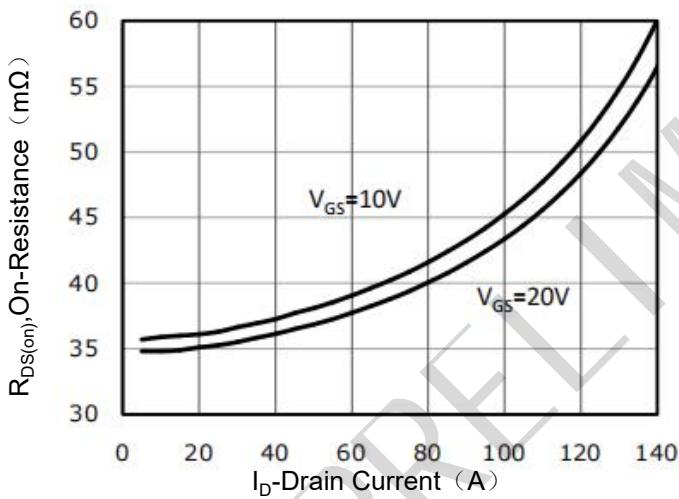
**Figure 1. Output Characteristics**



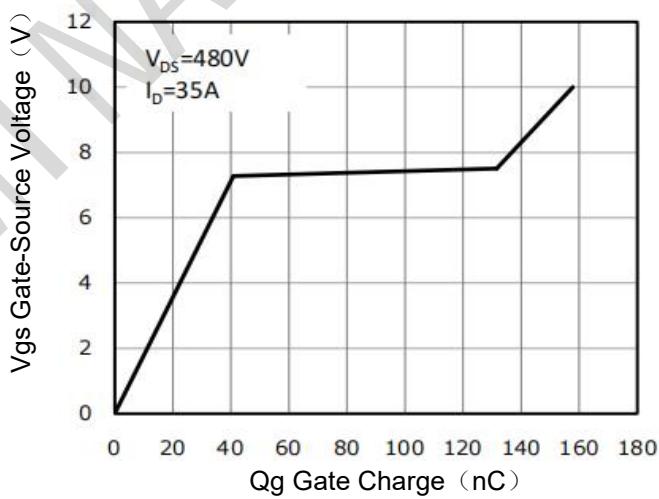
**Figure 2. Transfer Characteristics**



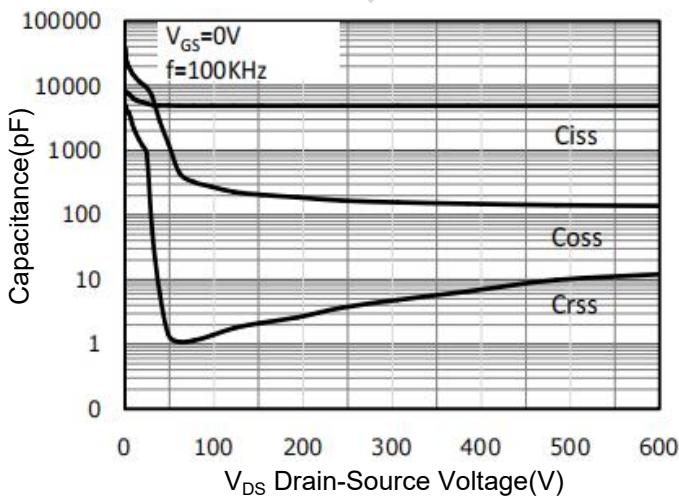
**Figure 3. Drain Source On Resistance**



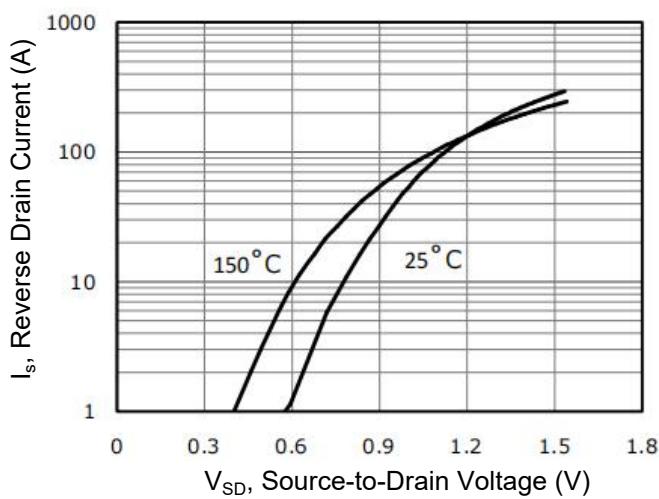
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

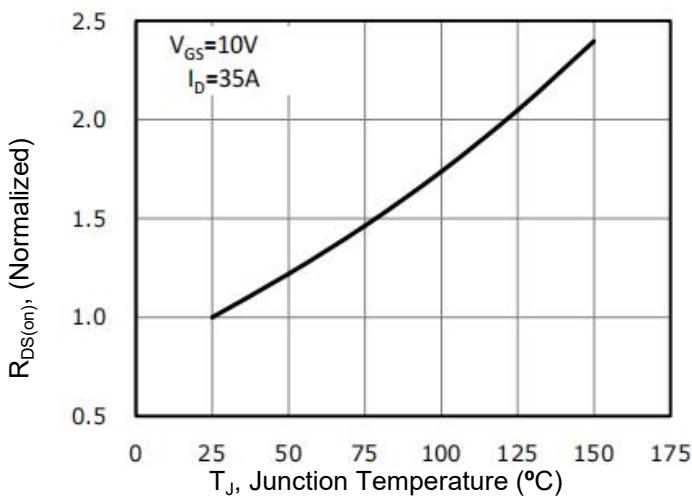


**Figure 6. Source-Drain Diode Forward**

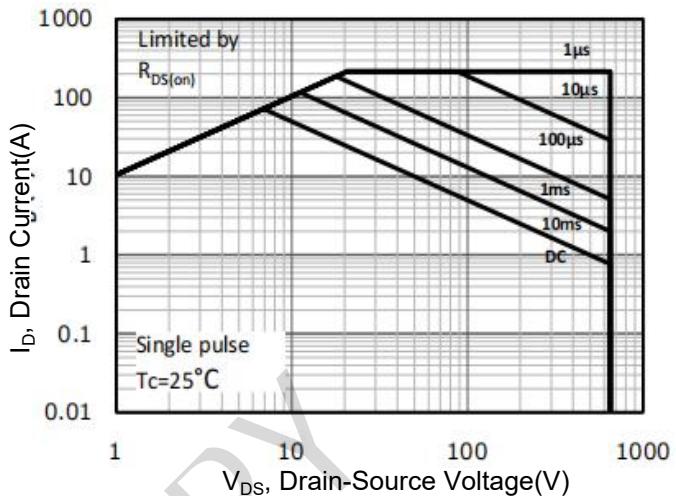


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

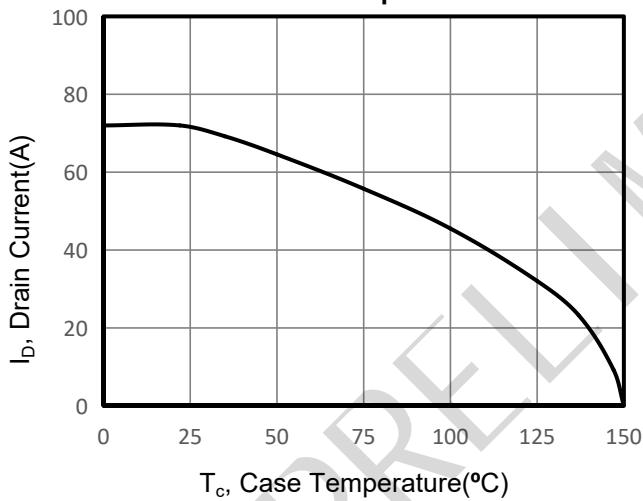
**Figure 7. Drain-Source On-Resistance**



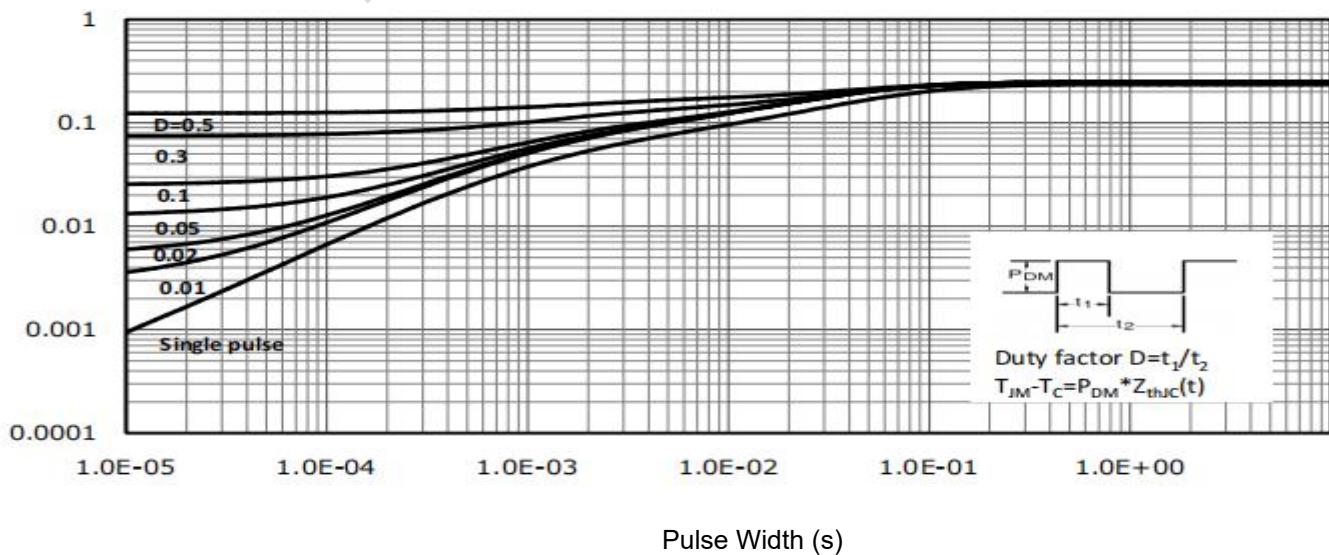
**Figure 8. Safe Operation Area**



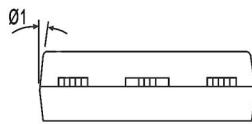
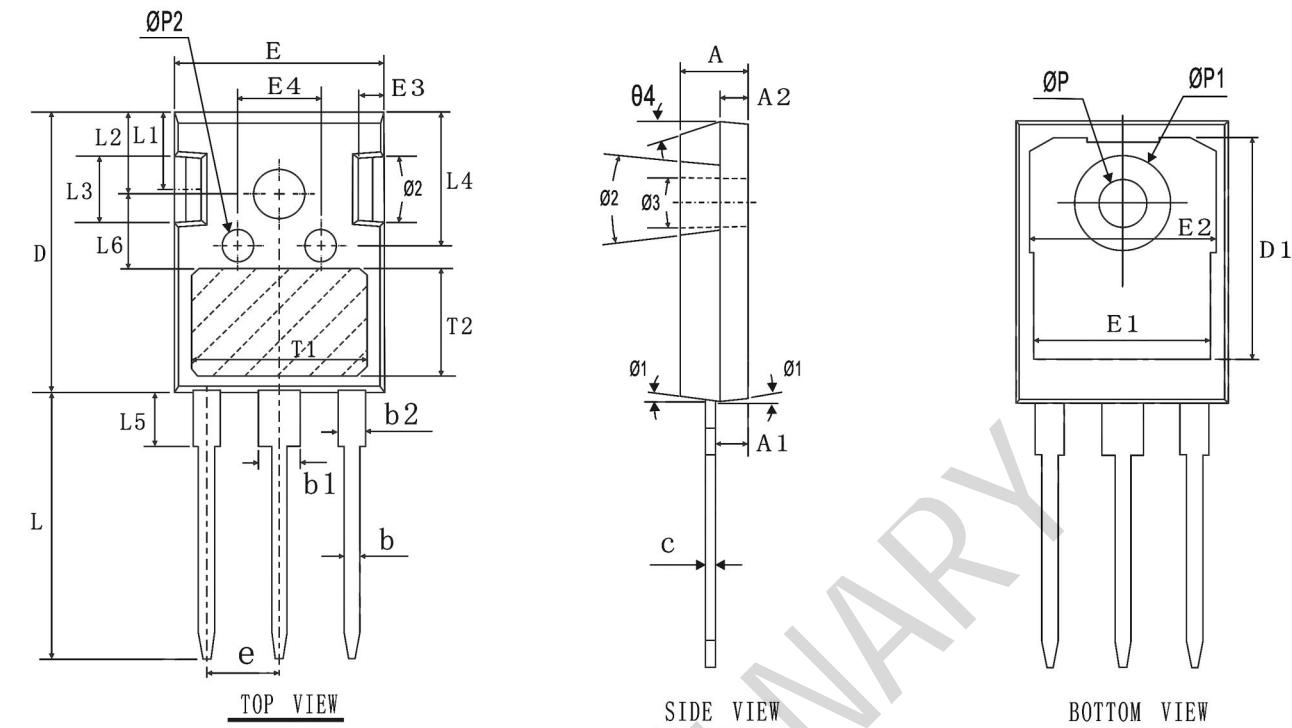
**Figure 9. Maximum Continuous Drain Current vs Case Temperature**



**Figure 10. Normalized Maximum Transient Thermal Impedance**



## TO-247 Package Information



COMMON DIMENSIONS  
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.20	2.40	2.60
A2	1.85	2.00	2.15
b	1.10	1.20	1.30
b1	2.80	3.00	3.20
b2	1.80	2.00	2.20
c	0.52	0.62	0.72
D	20.35	20.65	20.95
D1	16.35	16.55	16.75
E	15.50	15.80	16.10
E1	13.10	13.30	13.50
E2	13.80	14.00	14.20
E3	1.45	1.60	1.75
E4	6.00	6.20	6.40
L	19.80	20.00	20.20
L1	5.88	5.98	6.08
L2	5.88	5.98	6.08
L3	4.90	5.00	5.10
L4	9.70	9.80	9.90
L5	4.10	4.30	4.50
θ1	4°	7°	10°
θ2	11°	14°	17°
θ3	1°	--	2°
θ4	10°	15°	20°
ØP	3.35	3.60	3.85
ØP1	--	--	7.30
ØP2	2.25	2.50	2.75
e	5.44BSC		
T1	12.80REF		
T2	7.80REF		
L6	5.50REF		